

						PATENT NUMBER							
		Subclass	Class										
		ISSUE CLASSIFICATION											
<p style="text-align: center;"><b>U.S. UTILITY Patent Application</b></p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 25%; text-align: center; padding: 5px;">8J</td> <td style="width: 25%; text-align: center; padding: 5px;">O.I.P.E.</td> <td style="width: 25%; text-align: center; padding: 5px;">PATENT DATE</td> </tr> <tr> <td style="text-align: center; padding: 5px;">SCANNED</td> <td style="text-align: center; padding: 5px;">IKY</td> <td style="text-align: center; padding: 5px;">Q.A. XLT</td> </tr> </table>								8J	O.I.P.E.	PATENT DATE	SCANNED	IKY	Q.A. XLT
8J	O.I.P.E.	PATENT DATE											
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APPLICATION NO.		CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER							
09/986282		F	174	23	2601	I.PATE							
APPLICANTS	Yoshiaki Uematsu Shinji Manabe												
TITLE	Wiring board and a method for manufacturing the wiring board												
PTO-2040 12/99													

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Assistant Examiner)	(Date)		Amount Due	Date Paid
	(Primary Examiner)	(Date)			
	(Legal Instruments Examiner)	(Date)			

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